

IN THE CLAIMS

Please amend claims 1, 5, 8, 11, 15 and 18 as follows:

1 1. (Currently Amended) A method of designing a video signal processing
2 integrated circuit (IC), comprising the steps of:

3 providing the video signal processing IC with an envelope detector for detecting
4 and outputting an envelope of a frequency modulated (FM) video signal;

5 providing the video signal processing IC with a level variation switching circuit
6 for selectively changing an envelope level of the FM video signal; and

7 connecting an input of the level variation switching circuit to a control output of a
8 microprocessor so that ON/OFF switching control of the level variation switching circuit
9 is executed in response to a control data input from the microprocessor so as to
10 selectively control operation of the level variation switching circuit and changing of the
11 envelope level of the FM video signal.

1 2. (Original) The method according to claim 1, wherein the level variation
2 switching circuit reduces variation in the envelope level of the FM video signal according
3 to standard playback (SP) mode information and super long playback (SLP) mode
4 information, respectively, contained in the control data input from the microprocessor.

1 3. (Original) The method according to claim 1, wherein the level variation
2 switching circuit operates in dependence on a playback mode of a video cassette recorder.

1 4. (Original) The method according to claim 1, wherein the level variation
2 switching circuit has a resistor at an output terminal of the envelope detector.

1 5. (Currently Amended) A video signal processing integrated circuit (IC)
2 incorporating an envelope detecting circuit for detecting an envelope level of an FM
3 video signal, wherein the envelope detecting circuit comprises:

4 a peak detector for receiving the FM video signal and for detecting a peak value of
5 the FM video signal; and

6 a level switch having a first input connected to an output of the peak detector and
7 having a second input connected to a control output of a microprocessor for selectively
8 controlling the envelope level of the FM video signal according to mode information
9 applied from the microprocessor so as to selectively reduce a variation in the envelope
10 level in accordance with a type of mode of operation of a video cassette recorder (VCR).

1 6. (Original) The video signal processing IC according to claim 5, further
2 comprising an amplifier connected to an input terminal of the peak detector for
3 amplifying the FM video signal with a predetermined gain prior to provision to the peak
4 detector.

1 7. (Original) The video signal processing IC according to claim 5, further
2 comprising an amplifier connected to an output terminal of the peak detector for
3 amplifying the FM video signal with a predetermined gain after processing in the peak
4 detector.

1 8. (Currently Amended) A video signal processing integrated circuit (IC)
2 incorporating an envelope detecting circuit for detecting an envelope level of an FM
3 video signal, wherein the envelope detecting circuit comprises:

4 a peak detector for receiving the FM video signal and for detecting a peak value of
5 the FM video signal; and

6 a level switch connected to an output of the peak detector for selectively
7 controlling the envelope level of the FM video signal according to mode information
8 applied from a microprocessor so as to reduce a variation in the envelope level in
9 accordance with a type of mode of operation of a video cassette recorder (VCR);

10 wherein the level switch includes a resistance element having a first terminal
11 connected to the output of the peak detector and having a second terminal, and a
12 switching control element connected to the second terminal of the resistance element, the
13 switching control element being controlled by the mode information from the
14 microprocessor.

1 9. (Original) The video signal processing IC according to claim 8, wherein the
2 mode information comprises SP/SLP mode information relating to operation of the VCR.

1 10. (Original) The video signal processing IC according to claim 5, wherein the
2 mode information comprises SP/SLP mode information relating to operation of the VCR.

1 11. (Currently Amended) A method of designing a video signal processing

integrated circuit (IC) having an envelope detector for detecting an envelope of a frequency modulated (FM) video signal, said method comprising the steps of:

providing a level variation switching circuit in the video signal processing IC for changing an envelope level of the FM video signal according to an execution mode;

connecting an input of the level variation switching circuit to a control output of a microprocessor; and

selectively providing an ON/OFF switching control of the level variation switching circuit in response to a control data input from the microprocessor, said control data input containing playback mode information relative to the FM video signal, so as to selectively change the envelope level of the FM video signal.

12. (Original) The method according to claim 11, further comprising the step of providing the level variation switching circuit with a capability of reducing variation in the envelope level of the FM video signal according to standard playback (SP) mode information and super long playback (SLP) mode information, respectively, contained in the control data input from the microprocessor.

13. (Original) The method according to claim 11, wherein the level variation switching circuit operates in dependence on a playback mode of a video cassette recorder.

14. (Original) The method according to claim 11, wherein the level variation switching circuit has a resistor at an output terminal of the envelope detector.

1 15. (Currently Amended) A video signal processing circuit for detecting an
2 envelope level of an FM video signal input thereto, said circuit comprising:

3 peak detector means for receiving the FM video signal and for detecting a peak
4 value of the FM video signal; and

5 level switch means connected to said peak detector means for selectively
6 controlling the envelope level of the FM video signal according to playback mode
7 information relating to a mode of operation of a video cassette recorder (VCR), said
8 playback mode information being applied to said level switch means so as to selectively
9 reduce a variation in the envelope level in accordance with the mode of operation of the
10 video cassette recorder (VCR).

1 16. (Original) The video signal processing circuit according to claim 15, further
2 comprising an amplifier connected to an input terminal of said peak detector means for
3 amplifying the FM video signal with a predetermined gain prior to provision to said peak
4 detector means.

1 17. (Original) The video signal processing circuit according to claim 15, further
2 comprising an amplifier connected to an output terminal of said peak detector means for
3 amplifying the FM video signal with a predetermined gain after processing in said peak
4 detector means.

1 18. (Currently Amended) A video signal processing circuit for detecting an
2 envelope level of an FM video signal input thereto, said circuit comprising:

3 peak detector means for receiving the FM video signal and for detecting a peak
4 value of the FM video signal; and

5 level switch means connected to said peak detector means for selectively
6 controlling the envelope level of the FM video signal according to mode information
7 applied thereto so as to selectively reduce a variation in the envelope level in accordance
8 with a mode of operation of a video cassette recorder (VCR);

9 wherein said level switch means includes a resistance element having a first
10 terminal connected to said peak detector means and having a second terminal, said level
11 switch means further including a switching control element connected to the second
12 terminal of the resistance element, the switching control element being controlled by the
13 mode information applied to said level switch means.

1 19. (Original) The video signal processing circuit according to claim 18,
2 wherein the mode information comprises SP/SLP mode information relating to operation
3 of the VCR.

1 20. (Original) The video signal processing circuit according to claim 15,
2 wherein the mode information comprises SP/SLP mode information relating to operation
3 of the VCR.